

SYSTEMS AND METHODS FOR OPERATING LOGIC CIRCUITS

ABSTRACT OF THE DISCLOSURE

Systems and methods for reducing the power consumption of some combinations of logic gates by reducing the number of unnecessary transitions that are made by logic gates that do not affect the output of the logic. In one embodiment, a modified exclusive-OR (XOR) gate is coupled to a modified multiplexer. The XOR gate has two inputs, A_{in} and B_{in} , and an output, XOR_{out} , which is provided as an input to the multiplexer. Another input to the multiplexer is C_{in} . A select signal is input to the multiplexer to select either C_{in} or XOR_{out} to be provided at the output of the multiplexer. If XOR_{out} is selected, the XOR gate operates in a first mode in which it functions as a normal XOR gate. If C_{in} is selected, the XOR gate operates in a second mode in which the XOR gate uses less power than when the XOR gate operates normally.